CS320: Internetworking Fall 2021

Review Quiz #2

- Name only: _____
- Release date: Sept 15, 2021
- Total 5 pts

I. Explain the following four delays: processing delay, queuing delay, transmission delay, and propagation delay.

[l pt]

2. Suppose host A and B communicate over a circuit-switched network. The link's total trans. capacity is 1.536 Mbps, and each link uses TDM with 12 slots/sec. Before host A send a file of 640,000 bits to host B, it will take 1500 msec to establish end-to-end circuit. How long does it take to send a file of 640,000 bits from host A to host B? Show All your work.

[l pt]

[l pt]

[l pt]

3. What will cause packet loss at router?

4. What is the bottleneck link?

5. What are the differences between FDM and TDM?

[l pt]